

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: John Bruno
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Examiner: Nathan W. Ha
 Art Unit: 2814
 Our File No.: 00100.02.0038
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Title: **INTEGRATED CIRCUIT HAVING MEMORY DISPOSED THEREON AND
 METHOD OF MAKING THEREOF**

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8/25/04

Date

Themis Anagnos

OK X
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 10/12/04

Dear Sir:

In response to the Final Office Action mailed June 25, 2004, Applicant submits the following amendment and response.

Status of the Claims begin on page 2 of this paper.

Remarks begin on page 6 of this paper.

CHICAGO#1259160.1

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